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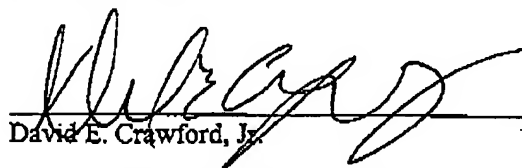
CLIENT/MATTER 09792909-5755

FROM David E. Crawford, Jr.

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Type of Paper transmitted: Preliminary Amendment D

Applicant's Name: Tsutomu IMOTO

Serial No. (Control No.): 10/689,305

Examiner: Kamand Cuneo

Filing Date October 20, 2003

Art Unit: 2841

Application Title: METHOD OF FABRICATING SEMICONDUCTOR DEVICE HAVING SEMICONDUCTOR RESISTANCE ELEMENT

Original will NOT be mailed

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:)	Group Art Unit: 2841
)	
Tsutomu IMOTO)	Examiner: Kamand Cuneo
)	
Application No. 10/689,305)	
)	
Filed: October 20, 2003)	
)	
For: Method of Fabricating Semiconductor)	
Device Having Semiconductor)	
Resistance)	

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT D

Dear Sir:

In response to the Examiner's request, please amend the above-identified application as follows: